

**IN THE ABSTRACT**

Please replace the Abstract with the new Abstract appearing on the following separate page:

**ABSTRACT[[:]] OF THE DISCLOSURE**

A data processing device is described ~~which at least comprises~~ having a master controller (1), a first functional unit (2) ~~which includes~~ including a slave controller (20), and a second functional unit (3). The functional units (2,3) share a common memory ~~means (11)~~. The device is programmed for executing an instruction by the first functional unit (2), the execution ~~of said instruction~~ involving input/output operations by the first functional unit (3), wherein output data of the first functional unit (2) is processed by the second functional unit (3) during said execution and/or the input data is generated by the second functional (3) unit during said execution.

Figure 1.